## What is claimed is:

- 1 1. A method comprising:
- 2 creating a first stack of tasks associated with a
- 3 first thread;
- 4 creating a second stack of tasks associated with
- 5 a second thread;
- 6 executing tasks on the first stack of tasks with
- 7 the first thread;
- 8 determining if the second stack of tasks contains
- 9 a queued task executable by the first thread; and
- 10 executing a queued task in the second stack by
- 11 the first thread.
  - 1 2. The method as in claim 1 further comprising
- 2 determining the second stack of tasks has a queued task
- 3 includes examining a bit mask.
- 1 3. The method as in claim 2 further comprising
- 2 locking the bit mask before the bit mask is examined.
- 1 4. The method as in claim 2 further comprising
- 2 searching the second stack of tasks to determine if the
- 3 second stack of tasks has a queued task.
- 1 5. The method as in claim 4 further comprising
- 2 locking the second stack of tasks by the first thread
- 3 before it is searched.

- 1 6. The method as in claim 2 further comprising
- 2 changing a bit in the bit mask associated with the second
- 3 thread if a queued task is not on the second stack of
- 4 tasks.
- 1 7. The method as in claim 1 further comprising
- 2 determining if the executed queued task was a taskq task.
- 1 8. The method as in claim 7 further comprising
- 2 changing a bit in a bit mask in response to executing a
- 3 taskq task which generates additional tasks.
- 1 9. The method as in claim 8 further comprising
- 2 providing a signal to another thread that an additional
- 3 task was generated.
- 1 10. The method as in claim 8 wherein changing the
- 2 bit in the bit mask includes changing a bit associated with
- 3 the second thread indicating the second stack of tasks
- 4 contains a task executable by the first thread.
- 1 11. The method as in claim 1 further comprising
- 2 executing all executable tasks on the first stack of tasks
- 3 before determining if the second stack of tasks contains a
- 4 queued task.

- 1 12. The method as in claim 11 further comprising
- 2 causing the first thread to enter a wait state if the
- 3 second stack of tasks does not contain a queued task
- 4 executable by the first thread.
- 1 13. The method as in claim 12 further comprising
- 2 causing the first thread to exit the wait state in response
- 3 to another thread executing a task generating task.
- 1 14. A method comprising:
- 2 creating a plurality of threads each having a
- 3 stack of queued tasks;
- 4 at least one thread executing tasks on its stack
- 5 of queued tasks until no queued task remains in its stack
- 6 of queued tasks that is executable by the thread and
- 7 thereby becoming an idle thread;
- 8 at least one idle thread searching a bit mask for
- 9 a bit that is set indicating a thread that may have a task
- 10 executable by an idle thread;
- in response to a set bit in the bit mask, at
- 12 least one idle thread searching the stack of queued tasks
- 13 owned by another thread for an available gueued task that
- 14 can be executed by the searching thread; and
- if an available executable task is found, then an
- 16 idle thread executes the available task.

- 1 15. The method as in claim 14 further comprising
- 2 changing a bit in the bit mask if an executable task is not
- 3 found.
- 1 16. The method as in claim 14 further comprising
- 2 setting a bit in the bit mask if the available executable
- 3 task is a task generating task which generates an
- 4 additional task.
- 1 17. The method as in claim 16 further comprising
- 2 enabling an idle thread to search its stack of gueued tasks
- 3 for an available task that is executable in response to the
- 4 setting of a bit in the bit mask.
- 1 18. The method as in claim 14 further comprising
- 2 queuing a task generated by the execution of a task
- 3 generating task on the stack of queued tasks from which the
- 4 task generating task was found.
- 1 19. The method as in claim 14 further comprising in
- 2 response to the idle thread executing an available
- 3 executable task, the idle thread searching its stack of
- 4 queued tasks for an available task that is executable.

- 1 20. The method as in claim 14 further comprising an
- 2 idle thread entering a wait state in response to the idle
- 3 thread not finding a bit set in the bit mask.
- 1 21. A machine-readable medium that provides
- 2 instructions, which when executed by a set of one or more
- 3 processors, enable the set of processors to perform
- 4 operations comprising:
- 5 creating a first stack of tasks associated with a
- 6 first thread;
- 7 creating a second stack of tasks associated with
- 8 a second thread;
- 9 executing tasks on the first stack of tasks with
- 10 the first thread;
- determining if the second stack of tasks contains
- 12 a queued task executable by the first thread; and
- executing a queued task in the second stack by
- 14 the first thread.
- 1 22. The machine-readable medium of claim 21 wherein
- 2 determining the second stack of tasks has a queued task is
- 3 determined, in part, by examining a bit mask, and in
- 4 response to a state of a bit in the bit mask, searching the
- 5 second stack of tasks for a queued task.

- 1 23. The machine-readable medium of claim 22 wherein
- 2 the bit mask has a bit associated with the second thread
- 3 and the bit is changed if a queued task is not on the
- 4 second stack of tasks.
- 1 24. The machine-readable medium of claim 21 further
- 2 comprising determining if the executed queued task was a
- 3 task generating task and changing a bit in the bit mask in
- 4 response to executing a task generating task that generates
- 5 an additional task.
- 1 25. The machine-readable medium of claim 24 wherein
- 2 changing the bit in the bit mask includes changing a bit
- 3 associated with the second thread indicating the second
- 4 stack of tasks contains a task executable by the first
- 5 thread.
- 1 26. The machine-readable medium of claim 24 further
- 2 comprising enabling the first thread to enter a wait state
- 3 if the second stack of tasks does not contain a queued task
- 4 executable by the first thread and enabling the first
- 5 thread to exit the wait state in response to another thread
- 6 executing a task-generating task.

- 1 27. An apparatus comprising:
- a memory including a shared memory location;
- a set of at least one processors executing at
- 4 least a first and second parallel thread;
- 5 the first thread having a first stack of tasks
- 6 and the second thread having a second stack of tasks; and
- 7 the first thread determines if a queued task
- 8 executable by the first thread is available on the second
- 9 stack of tasks and the first thread executes an available
- 10 task on the second stack of tasks.
  - 1 28. The apparatus as in claim 27 wherein the first
  - 2 thread examines a bit mask to determine if the second stack
- 3 of tasks has an available task and then searches the second
- 4 stack of tasks for an available task.
- 1 29. The apparatus as in claim 28 wherein the first
- 2 thread changes a bit in the bit mask associated with the
- 3 second thread if the first thread executes an available
- 4 task in the second stack that generates a task.
- 1 30. The apparatus as in claim 27 wherein if the first
- 2 thread determines the second stack of tasks does not
- 3 contain an available task, the first thread enters a wait
- 4 state until a signal coupled to the first thread indicates
- 5 an available task may be available.